

UNITED STATES PATENT AND TRADEMARK OFFICE
CERTIFICATE OF CORRECTION

PATENT NO. : 6,888,748 B2
DATED : May 3, 2005
INVENTOR(S) : Greg A. Blodgett

Page 1 of 1

It is certified that error appears in the above-identified patent and that said Letters Patent is hereby corrected as shown below:

Title page,

Item [56], **References Cited**, OTHER PUBLICATIONS, "FET Programmable Read Only Memory" reference, "Disclosure Bull tin" should read -- Disclosure Bulletin --;

Column 1,

Line 27, "thousand ohms" should read -- thousands of ohms --;

Column 4,

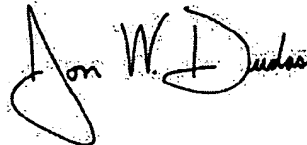
Line 31, "produce" should read -- produces --;

Column 5,

Line 43, "input" should read -- output --.

Signed and Sealed this

Second Day of August, 2005

A handwritten signature in black ink, appearing to read "Jon W. Dudas". The signature is stylized with a large, looped initial "J" and a distinct "D".

JON W. DUDAS
Director of the United States Patent and Trademark Office